ENGINEERING GRADUATE TRAINEE (GT) PROGRAM

DESCRIPTION:

As a Graduate Trainee of our Engineering Program, you will be involved in design development services in integrated circuit/ semiconductor design, embedded systems design, and embedded software development. You will collaborate with engineers to deliver technical solutions. Enjoy the real-world exposure with hands-on experience, on-the-job training and technical skills over 12 months period.

SPECIALISING AREA:

- Physical Design
- Design Verification
- Post – Silicon Validation

REQUIREMENTS:

- Degree in Electrical & Electronics Engineering/ Computer Engineering/ Software Engineering/ Electronics & Communications/ Computer Science
- Candidate with a minimum CGPA of 3.00 and above
- Strong foundation in Physical Design/Design Verification/Post-Silicon Validation
- Excellent problem solving, technical, and interpersonal skills
- Strong communication and teamwork abilities.
- Passion for learning and a desire to contribute to innovative projects.